# Implicitly Dealiased Convolutions on Shared-Memory and Distributed-Memory Parallel Processors 

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## Discrete Cyclic Convolution

- The FFT provides an efficient tool for computing the discrete cyclic convolution

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- The fast Fourier transform method exploits the properties that $\zeta_{N}^{r}=\zeta_{N / r}$ and $\zeta_{N}^{N}=1$.
- However, the pseudospectral method requires a linear convolution.
- The unnormalized backwards discrete Fourier transform of $\left\{F_{k}: k=0, \ldots, N\right\}$ is

$$
f_{j} \doteq \sum_{k=0}^{N-1} \zeta_{N}^{j k} F_{k} \quad j=0, \ldots, N-1
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- The orthogonality of this transform pair follows from

$$
\sum_{j=0}^{N-1} \zeta_{N}^{\ell j}= \begin{cases}N & \text { if } \ell=s N \text { for } s \in \mathbb{Z} \\ \frac{1-\zeta_{N}^{N N}}{1-\zeta_{N}^{\ell}}=0 & \text { otherwise }\end{cases}
$$

## Convolution Theorem

$$
\begin{aligned}
\sum_{j=0}^{N-1} f_{j} g_{j} \zeta_{N}^{-j k} & =\sum_{j=0}^{N-1} \zeta_{N}^{-j k}\left(\sum_{p=0}^{N-1} \zeta_{N}^{j p} F_{p}\right)\left(\sum_{q=0}^{N-1} \zeta_{N}^{j q} G_{q}\right) \\
& =\sum_{p=0}^{N-1} \sum_{q=0}^{N-1} F_{p} G_{q} \sum_{j=0}^{N-1} \zeta_{N}^{(-k+p+q) j} \\
& =N \sum_{s} \sum_{p=0}^{N-1} F_{p} G_{k-p+s N}
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- If only the first $m$ entries of the input vectors are nonzero, aliases can be avoided by zero padding input data vectors of length $m$ to length $N \geq 2 m-1$.
- Explicit zero padding prevents mode $m-1$ from beating with itself and wrapping around to contaminate mode $N=0 \bmod N$.
- Since FFT sizes with small prime factors in practice yield the most efficient implementations, the padding is normally extended to $N=2 m$ :

$$
\left\{F_{k}\right\}_{k=0}^{m-1}
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- If $F_{k}=0$ for $k \geq m$, one can easily avoid looping over the unwanted zero Fourier modes by decimating in wavenumber:

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f_{2 \ell} & =\sum_{k=0}^{m-1} \zeta_{2 m}^{2 \ell k} F_{k}=\sum_{k=0}^{m-1} \zeta_{m}^{\ell k} F_{k} \\
f_{2 \ell+1} & =\sum_{k=0}^{m-1} \zeta_{2 m}^{(2 \ell+1) k} F_{k}=\sum_{k=0}^{m-1} \zeta_{m}^{\ell k} \zeta_{2 m}^{k} F_{k}, \quad \ell=0,1, \ldots m-1
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- This requires computing two subtransforms, each of size $m$, for an overall computational scaling of order $2 m \log _{2} m=$ $N \log _{2} m$.
- Odd and even terms of the convolution can then be computed separately, multiplied term-by-term, and transformed again to Fourier space:

$$
\begin{aligned}
2 m F_{k} & =\sum_{j=0}^{2 m-1} \zeta_{2 m}^{-k j} f_{j} \\
& =\sum_{\ell=0}^{m-1} \zeta_{2 m}^{-k 2 \ell} f_{2 \ell}+\sum_{\ell=0}^{m-1} \zeta_{2 m}^{-k(2 \ell+1)} f_{2 \ell+1} \\
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- A 1D implicitly padded convolution is implemented in our FFTW++ library.
- Odd and even terms of the convolution can then be computed separately, multiplied term-by-term, and transformed again to Fourier space:

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- No bit reversal is required at the highest level.
- A 1D implicitly padded convolution is implemented in our FFTW++ library.
- This in-place convolution was written to use six out-of-place transforms, thereby avoiding bit reversal at all levels.
- The computational complexity is $6 \mathrm{Km} \log _{2} m$.
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- The numerical error is similar to explicit padding and the memory usage is identical.
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Input: vector f , vector g
Output: vector $f$
$\mathrm{u} \leftarrow \mathrm{fft}^{-1}(\mathrm{f})$;
$\mathrm{v} \leftarrow \mathrm{fft}^{-1}(\mathrm{~g})$;
$\mathrm{u} \leftarrow \mathrm{u} * \mathrm{v}$;
for $k=0$ to $m-1$ do
$\mathrm{f}[k] \leftarrow \zeta_{2 m}^{k} \mathrm{f}[k] ;$
$\mathrm{g}[k] \leftarrow \zeta_{2 m}^{k} \mathrm{~g}[k] ;$
end
$\mathrm{v} \leftarrow \mathrm{fft}^{-1}(\mathrm{f})$;
$\mathrm{f} \leftarrow \mathrm{fft}^{-1}(\mathrm{~g})$;
$\mathrm{v} \leftarrow \mathrm{v} * \mathrm{f}$;
$\mathrm{f} \leftarrow \mathrm{fft}(\mathrm{u})$;
$\mathrm{u} \leftarrow \mathrm{fft}(\mathrm{v})$;
for $k=0$ to $m-1$ do
$\mathbf{f}[k] \leftarrow \mathbf{f}[k]+\zeta_{2 m}^{-k} \mathbf{u}[k] ;$
end
return $\mathrm{f} /(2 \mathrm{~m})$;

## Implicit Padding in 1D



## Convolutions in Higher Dimensions

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## Recursive Convolution

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$$
\mathcal{F}_{N_{1}, \ldots, N_{d}} \longrightarrow \text { multiply } \longrightarrow \mathcal{F}_{N_{1}, \ldots, N_{d}}^{-1}
$$

- The technique of recursive convolution allows one to avoid computing and storing the entire Fourier image of the data:

$$
\mathcal{F}_{N_{d}} \longrightarrow N_{d} \times \text { convolve }_{N_{1}, \ldots, N_{d-1}} \longrightarrow \mathcal{F}_{N_{d}}^{-1}
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## Implicit Padding in 2D

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## Implicit Padding in 2D



## Implicit Padding in 3D



## Centered (Pseudospectral) Convolutions

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\sum_{p=k-m+1}^{m-1} f_{p} g_{k-p}
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- The ratio $(2 m-1) /(3 m-2)$ of the number of physical to total modes is asymptotic to $2 / 3$ for large $m$.
- A Hermitian convolution arises since the input vectors are real:

$$
f_{-k}=\overline{f_{k}} .
$$

## Hermitian Convolution

- The backwards implicitly padded centered Hermitian transform appears as

$$
u_{3 \ell+r}=\sum_{k=0}^{m-1} \zeta_{m}^{\ell k} w_{k, r}
$$

where

$$
w_{k, r} \doteq \begin{cases}U_{0}+\operatorname{Re} \zeta_{3}^{-r} U_{-m} & \text { if } k=0 \\ \zeta_{3 m}^{r k}\left(U_{k}+\zeta_{3}^{-r} \frac{U_{m-k}}{}\right) & \text { if } 1 \leq k \leq m-1\end{cases}
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- We exploit the Hermitian symmetry $w_{k, r}=\overline{w_{m-k, r}}$ to reduce the problem to three complex-to-real Fourier transforms of the first $c+1$ components of $w_{k, r}$ (one for each $r=-1,0,1$ ), where $c \doteq\lfloor m / 2\rfloor$ zeros.


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- Unrolling the loop to process four inputs and outputs simultaneously allows loop independence to be achieved, significantly improving performance in both the serial and parallel contexts.
- As a result, even in 1D, implicit dealiasing of pseudospectral convolutions is now significantly faster than explicit zero padding [Roberts \& Bowman 2016].

Hermitian Convolution for $m=2 c$


Hermitian Convolution for $m=2 c+1$


## 1D Implicit Hermitian Convolution



## Distributed-Memory Parallelization

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- We have compared several distributed matrix transpose algorithms, both blocking and nonblocking, under pure MPI and hybrid MPI/OpenMP architectures.
- Local transposition is not required within a single MPI node.
- We have developed an adaptive algorithm, dynamically tuned to choose the optimal block size.
$8 \times 8$ Block Transpose over 8 processors

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## Advantages of Hybrid MPI/OpenMP

- Use hybrid OpenMPI/MPI with the optimal number of threads:
- yields larger communication block size;
- local transposition is not required within a single MPI node;
- allows smaller problems to be distributed over a large number of processors;
- for 3D FFTs, allows for more slab-like than pencil-like models, reducing the size of or even eliminating the need for a second transpose;
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- sometimes more efficient (by a factor of 2) than pure MPI.
- The use of nonblocking MPI communications allows us to overlap computation with communication: this can yield up to an additional $32 \%$ performance gain for implicitly dealiased convolutions, for which a natural parallelism exists between communication and computation.


## Pure MPI 2D Convolutions



## Pure MPI 3D Convolutions



MPI 3D Implicit Parallel Efficiency


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- Direct transposition involves $P-1$ communications per process, each of size $N^{2} / P^{2}$, for a total per-process data transfer of

$$
\frac{P-1}{P^{2}} N^{2} .
$$

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- Outer: Over each team of $a$ processes, transpose the $a \times a$ matrix of $N / a \times M / a$ blocks.


## Communication Costs

- Let $\tau_{\ell}$ be the typical latency of a message and $\tau_{d}$ be the time required to send each matrix element, so that the time to send a message consisting of $n$ matrix elements is

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T_{D}=\tau_{\ell}(P-1)+\tau_{d} \frac{P-1}{P^{2}} N M=(P-1)\left(\tau_{\ell}+\tau_{d} \frac{N M}{P^{2}}\right)
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whereas a block transpose requires

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T_{B}(a)=\tau_{\ell}\left(a+\frac{P}{a}-2\right)+\tau_{d}\left(2 P-a-\frac{P}{a}\right) \frac{N M}{P^{2}}
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$$

- Let $L=\tau_{\ell} / \tau_{d}$ be the effective communication block length.


## Direct vs. Block Transposes

- Since

$$
T_{D}-T_{B}=\tau_{d}\left(P+1-a-\frac{P}{a}\right)\left(L-\frac{N M}{P^{2}}\right),
$$

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- For $N M<P^{2} L$, we see that $T_{B}$ is convex, with a minimum at $a=\sqrt{P}$.


## Optimal Number of Threads

- The minimum value of $T_{B}$ is

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\begin{aligned}
T_{B}(\sqrt{P}) & =2 \tau_{d}(\sqrt{P}-1)\left(L+\frac{N M}{P^{3 / 2}}\right) \\
& \sim 2 \tau_{d} \sqrt{P}\left(L+\frac{N M}{P^{3 / 2}}\right), \quad P \gg 1 .
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- If the matrix dimensions satisfy $N M>L$, as is typically the case, this minimum occurs above the transition value $(N M / L)^{1 / 2}$.


## Transpose Communication Costs



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- The hybrid paradigm provides an optimal setting for nonlocal computationally intensive operations found in applications like the fast Fourier transform.
- The advent of implicit dealiasing of convolutions makes overlapping transposition with FFT computation feasible.
- Writing of a high-performance dealiased pseudospectral code is now a relatively straightforward exercise. For example, see the protodns project at
http://github.com/dealias/dns


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